



10/747692

cd/c

Attorney Docket No. 1095.1294

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of:

Daisuke NAKATA

ATTENTION

U.S. Patent No.: 6,970,381

CERTIFICATE OF CORRECTION

Issued: November 29, 2005

BRANCH

For: SEMICONDUCTOR MEMORY

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

FEB 22 2006

of Correction

Sir:

Patentee respectfully requests that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

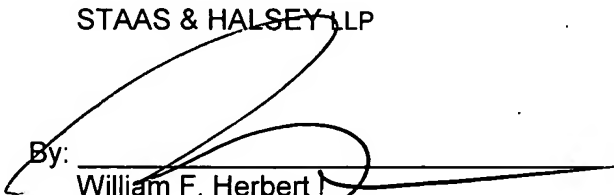
Respectfully submitted,

STAAS & HALSEY LLP

Date:

2/17/06

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,970,381

DATED : November 29, 2005

INVENTOR : Daisuke NAKATA

Column 12, line 17, change "5" to -4-.

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PATENT NO. 6,970,381

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